

1/13

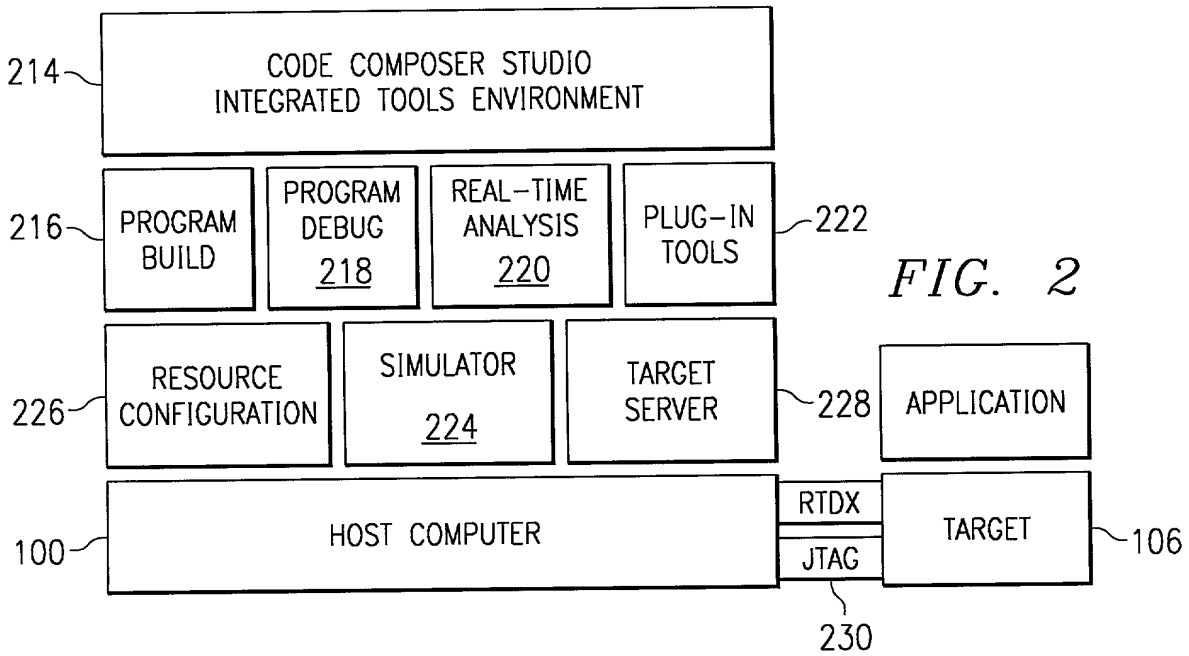
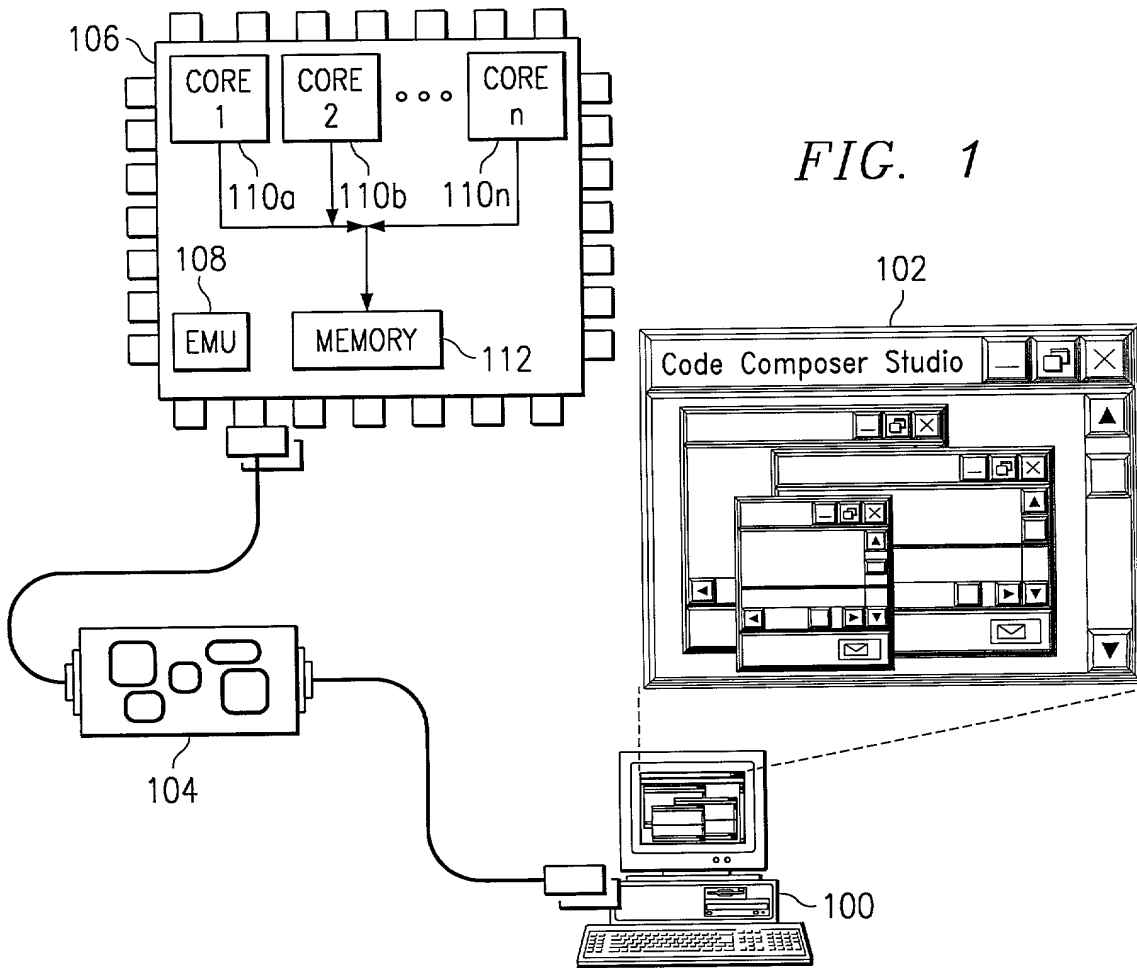
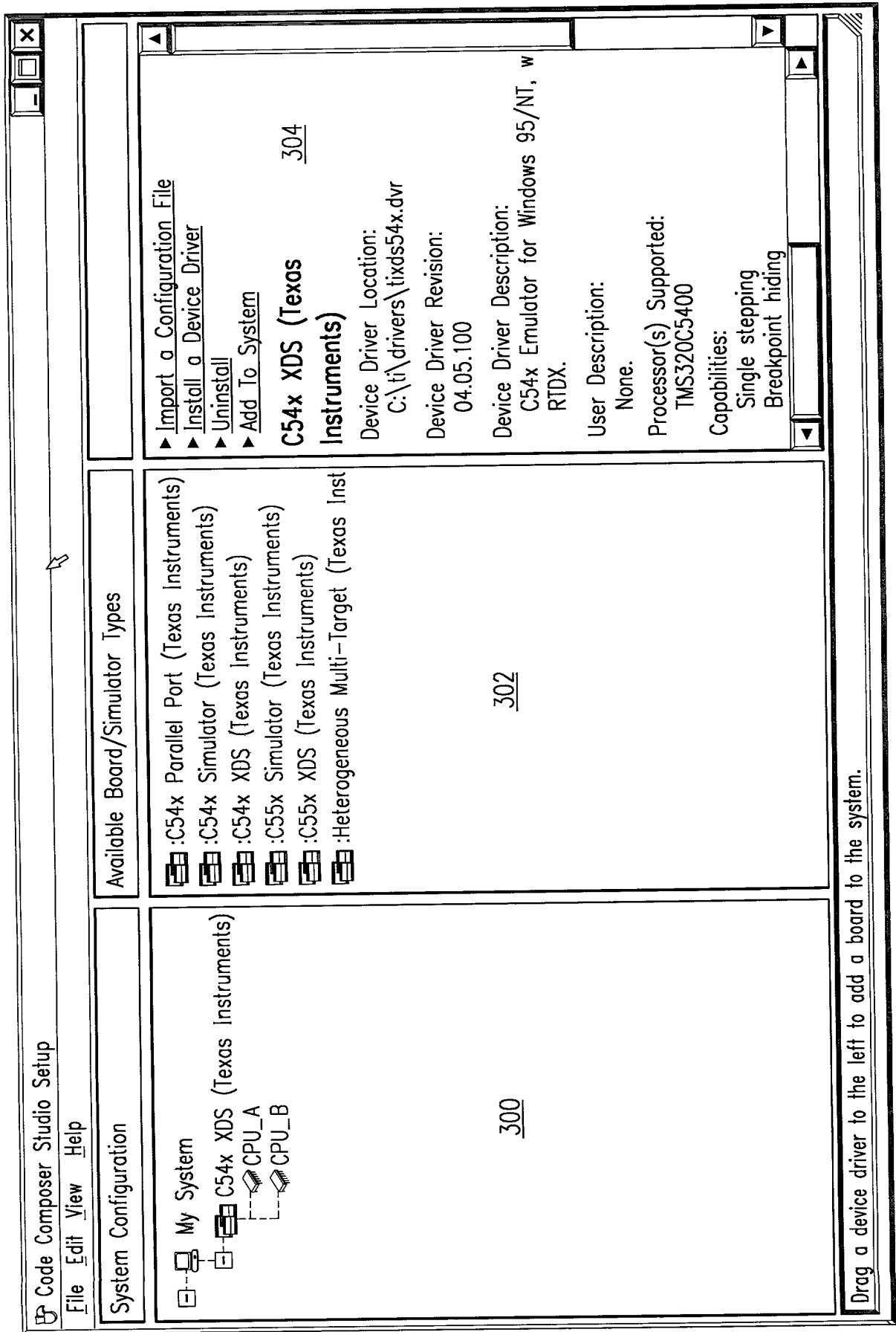


FIG. 3



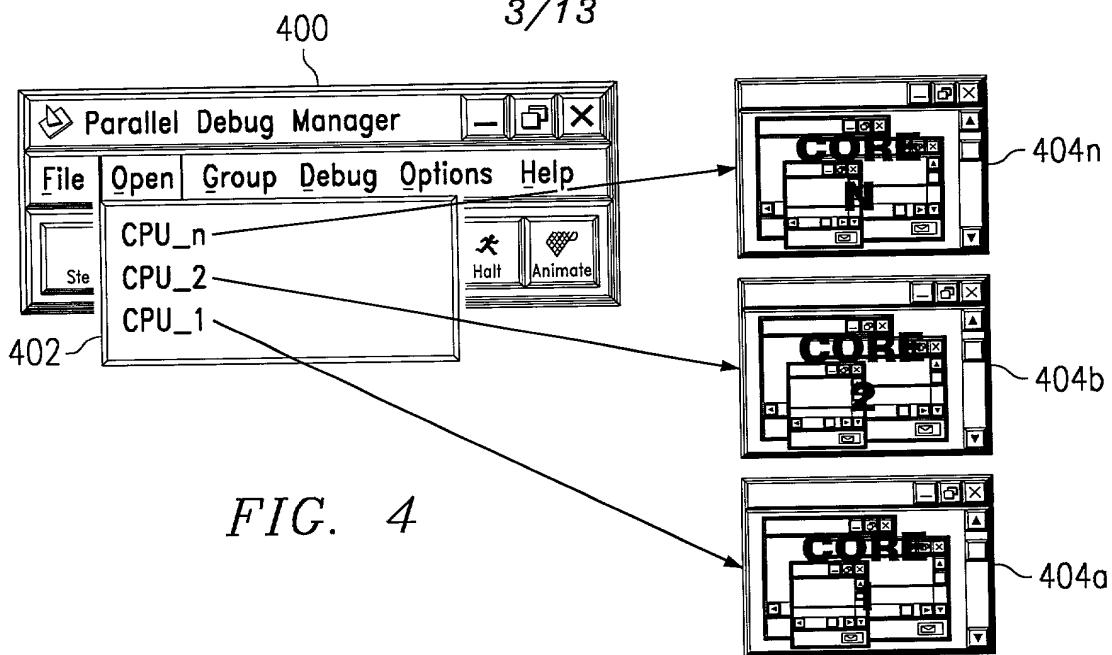


FIG. 4

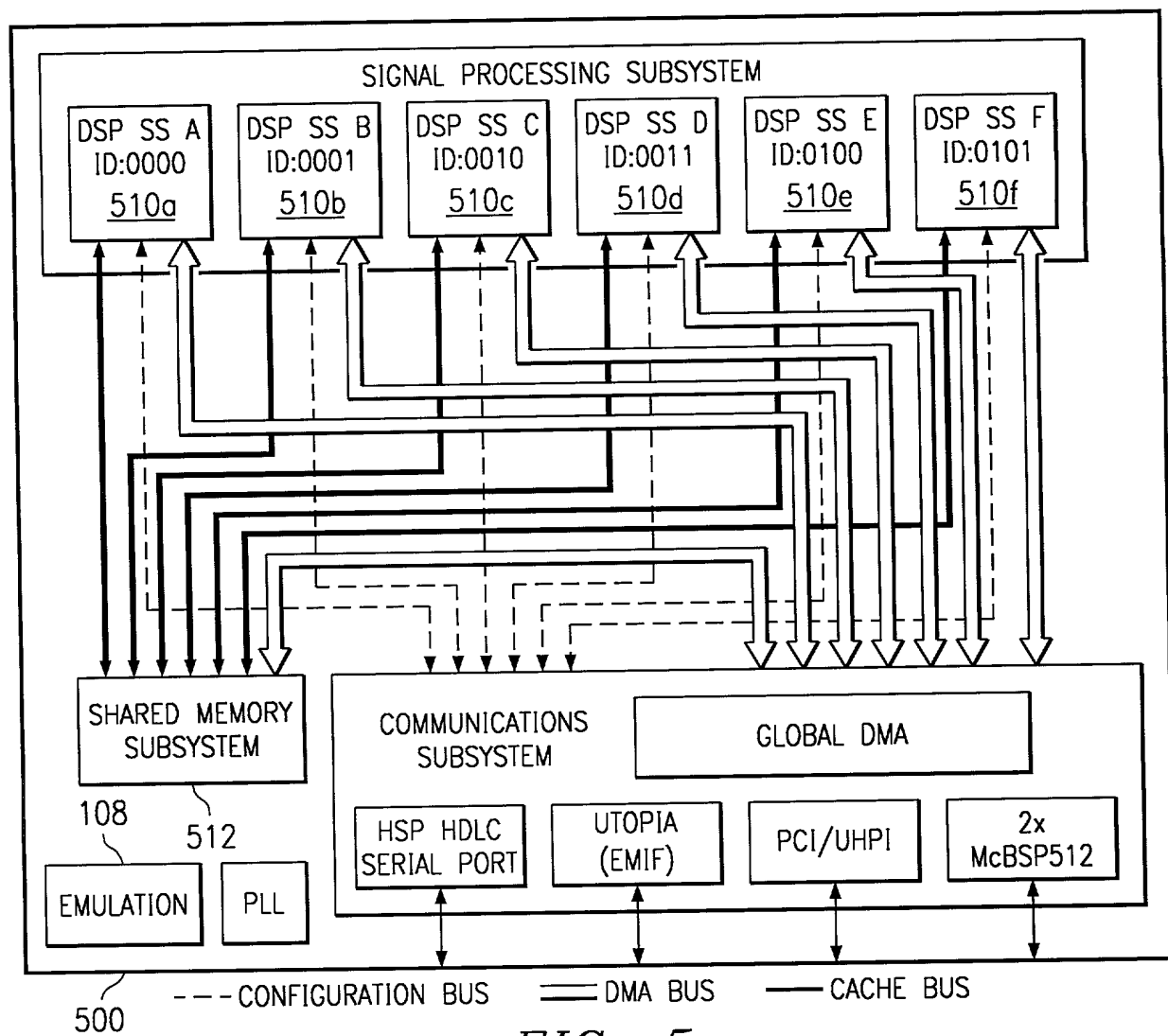


FIG. 5

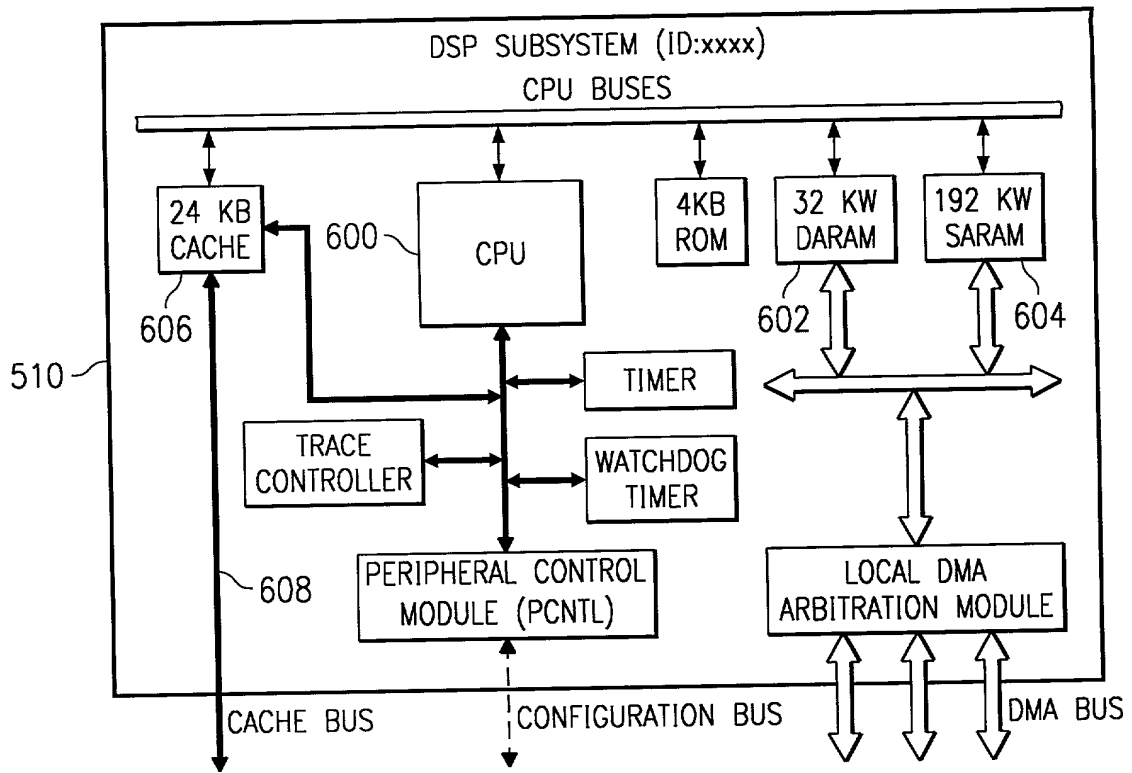
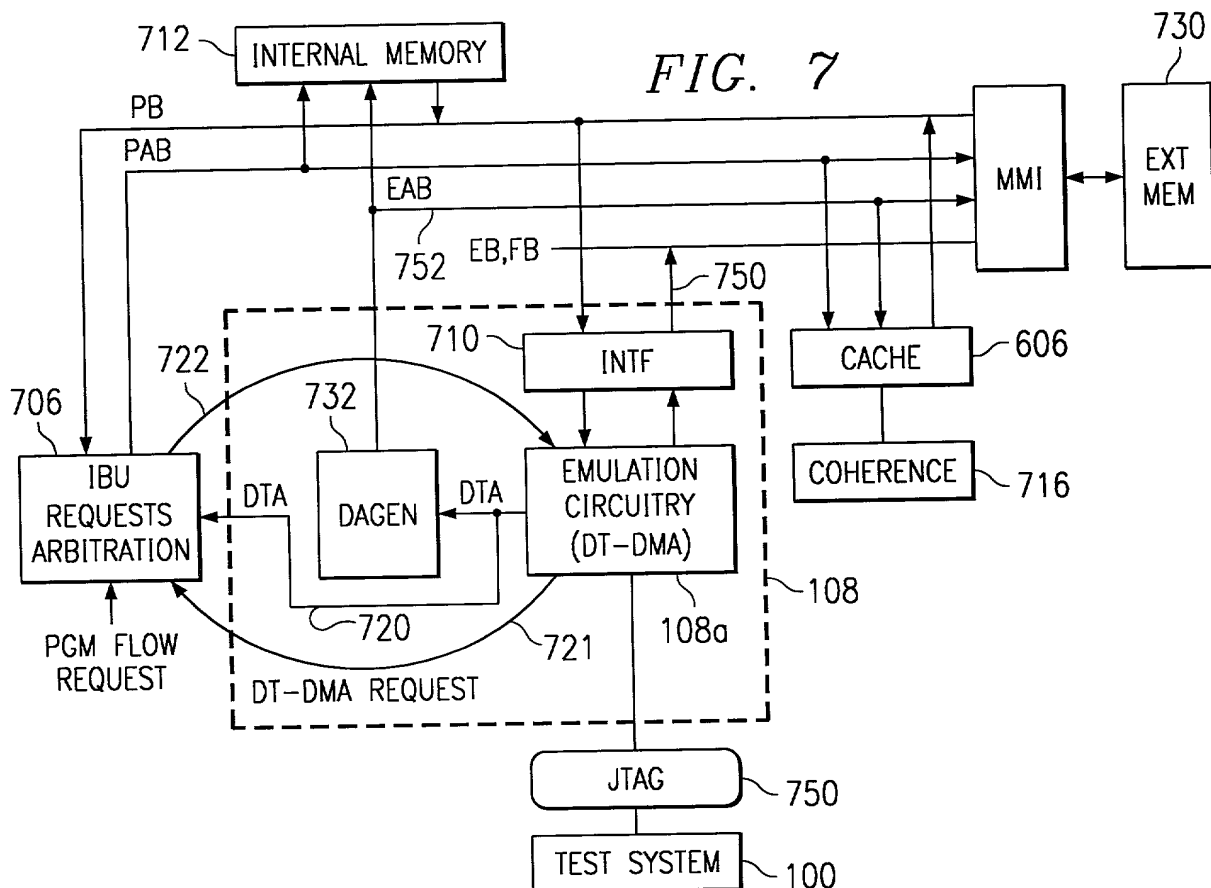


FIG. 7



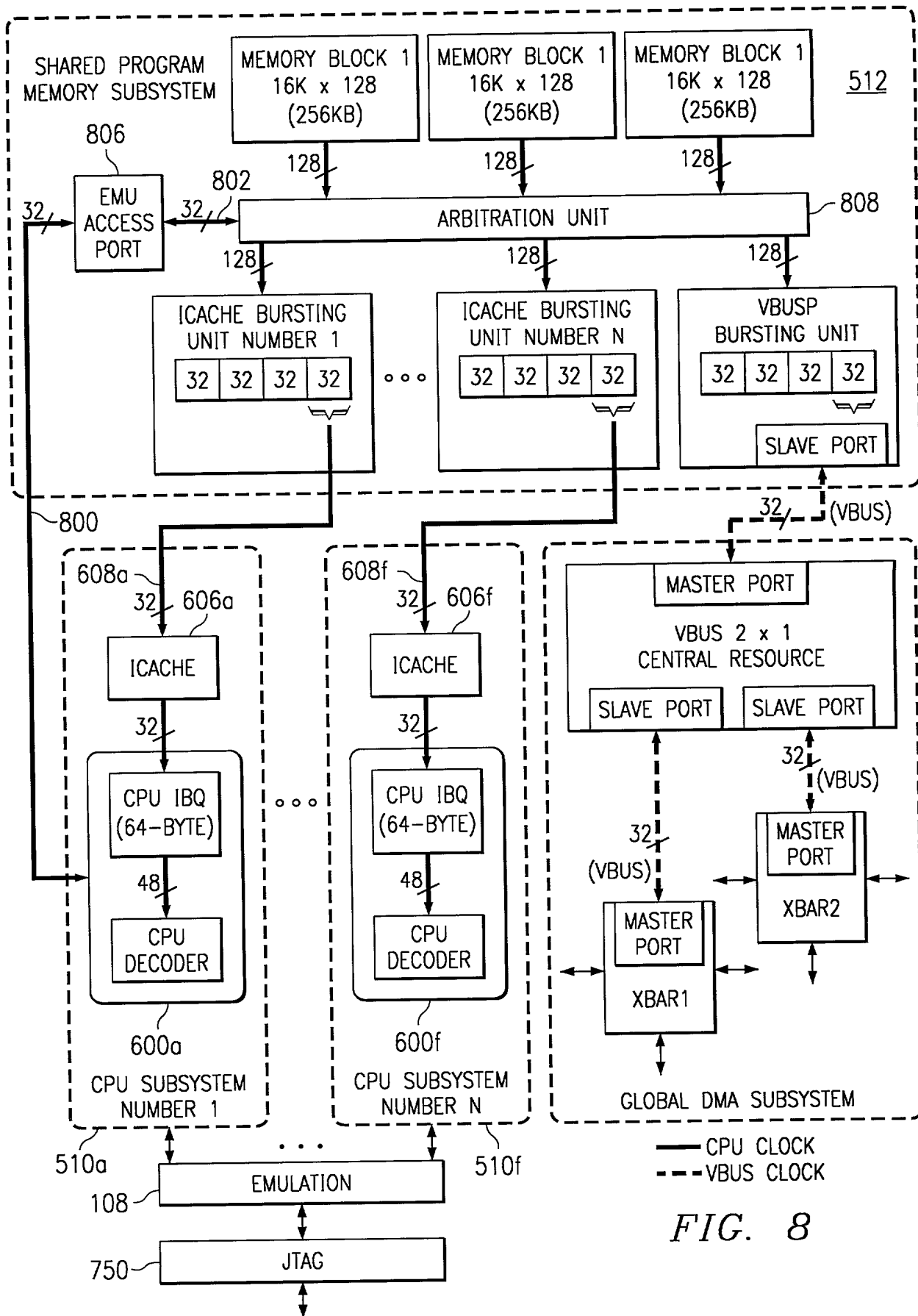
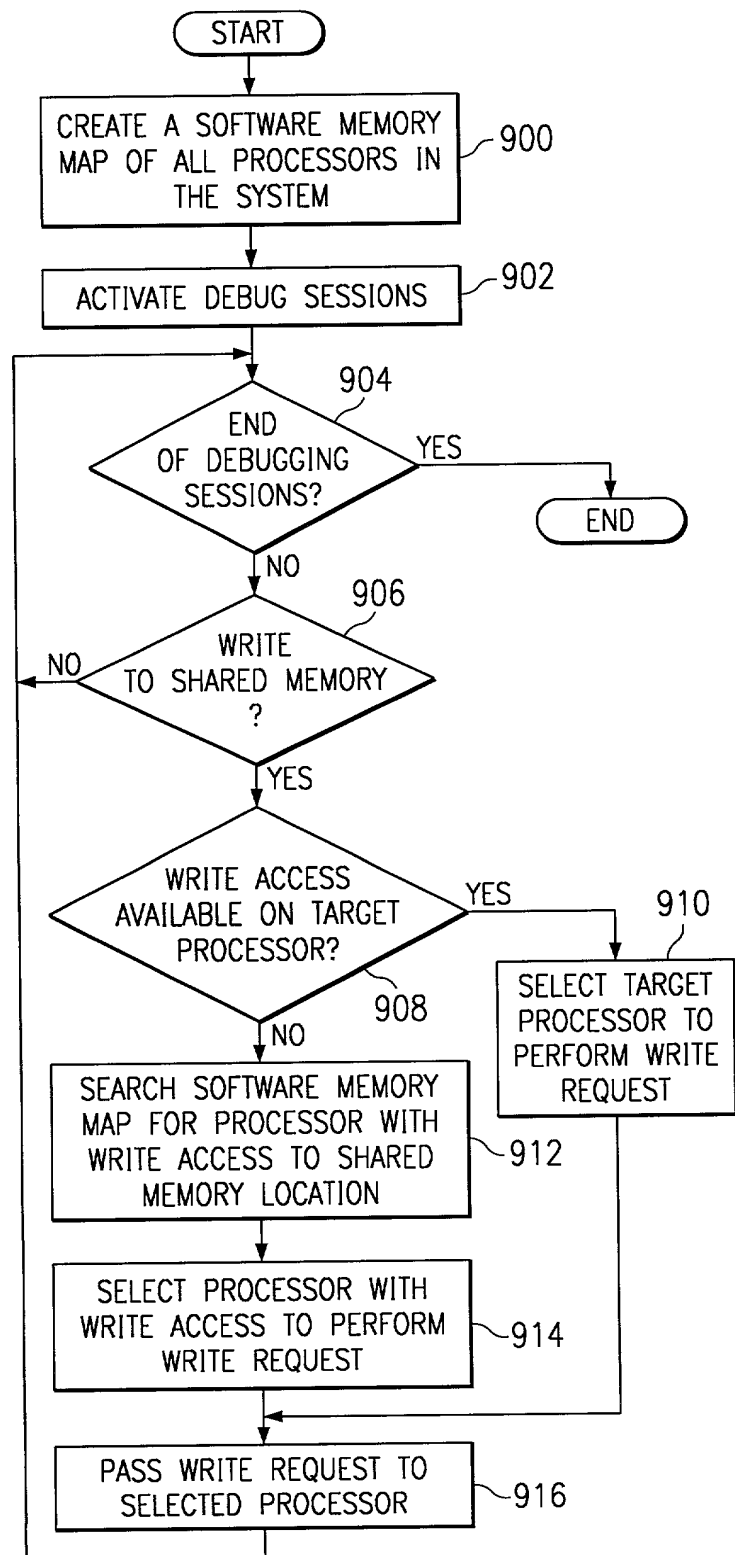


FIG. 8

FIG. 9



BYTE ADDRESS

000000h	LOCAL MEMORY MAPPED REGISTERS
00009Fh	
0000A0h	LOCAL DARAM (64KBYTE) DATA
00FFFFh	
010000h	LOCAL SARAM (64KBYTE) DATA/PROG
01FFFFh	
020000h	LOCAL SARAM (320KBYTE) DATA
06FFFFh	
070000h	RESERVED
F3FFFFh	
F40000h	SHARED SARAM (768KBYTE) PROG
1000	
FFF000h	LOCAL ROM (4KBYTE)
FFFFFFh	

FIG. 10

FIG. 11

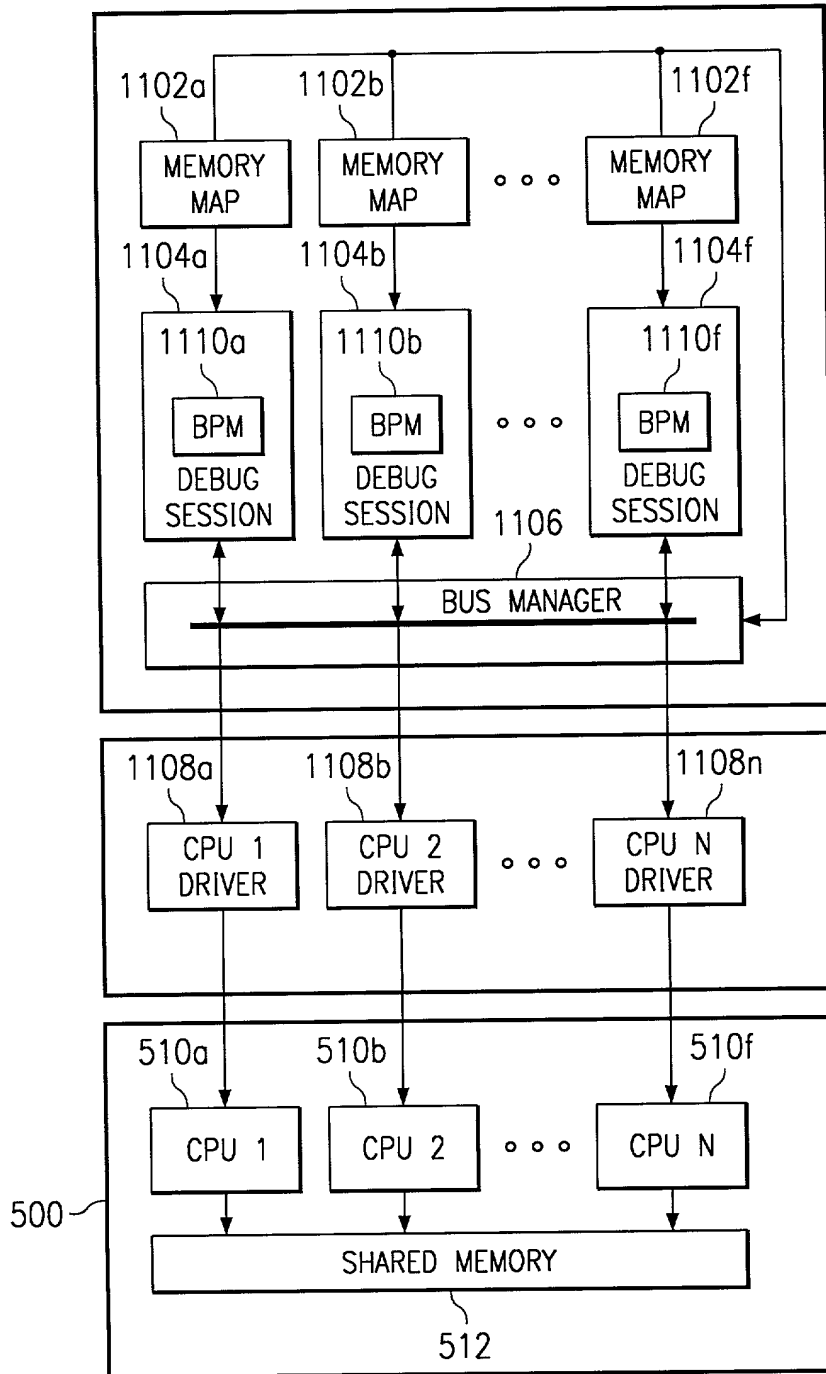


FIG. 12A

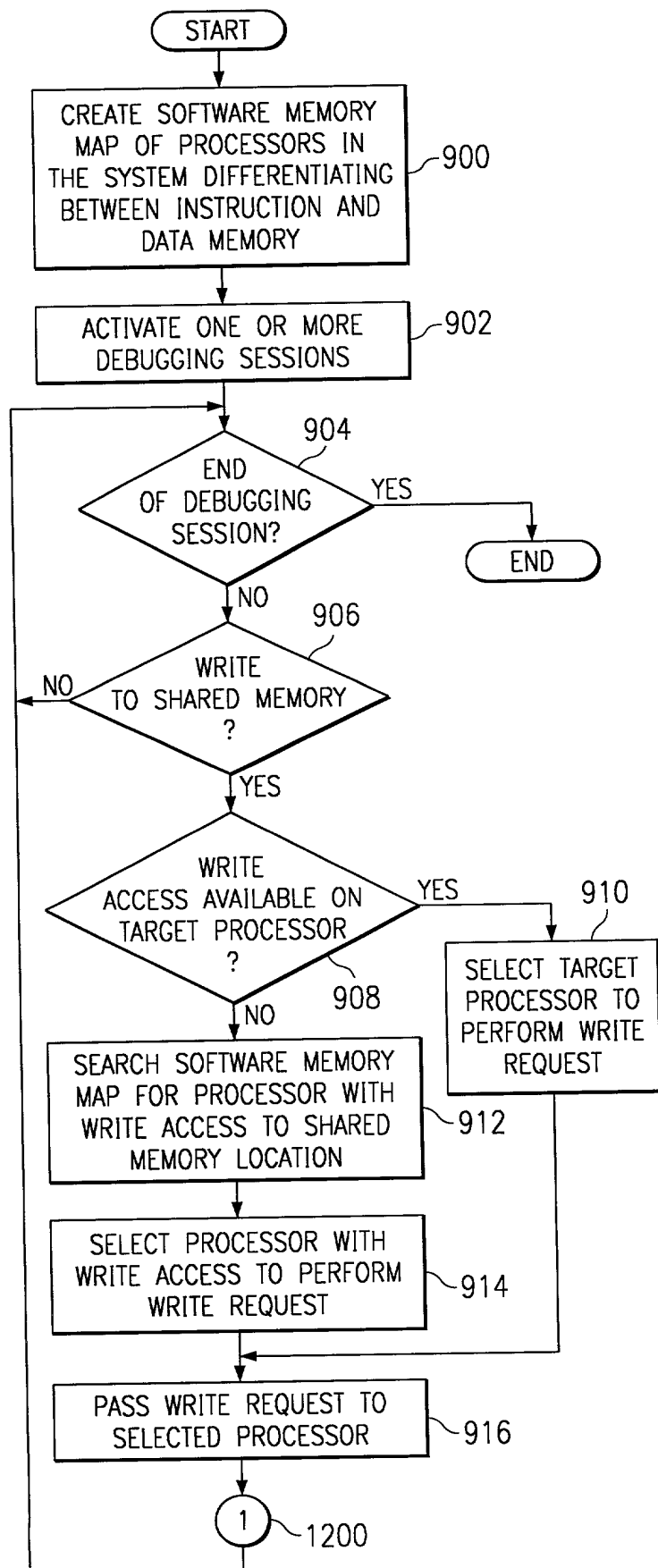


FIG. 12B

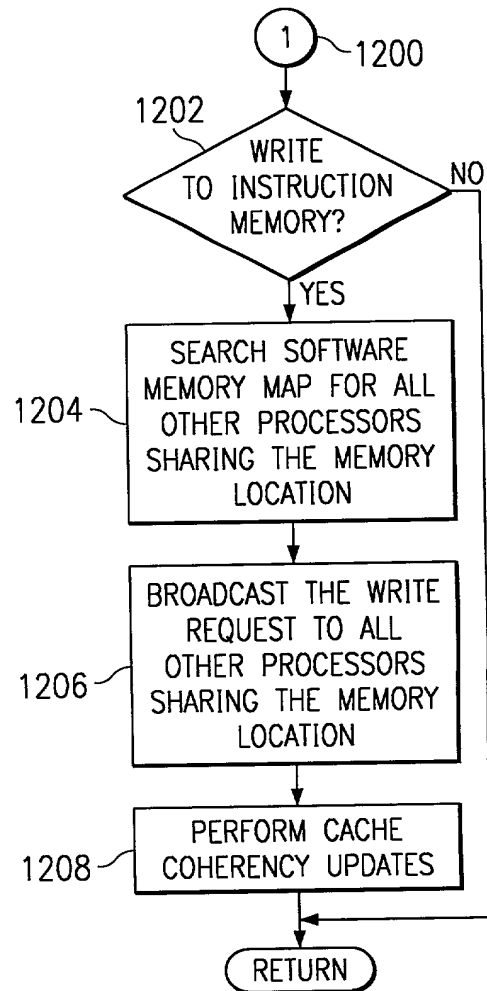


FIG. 13A

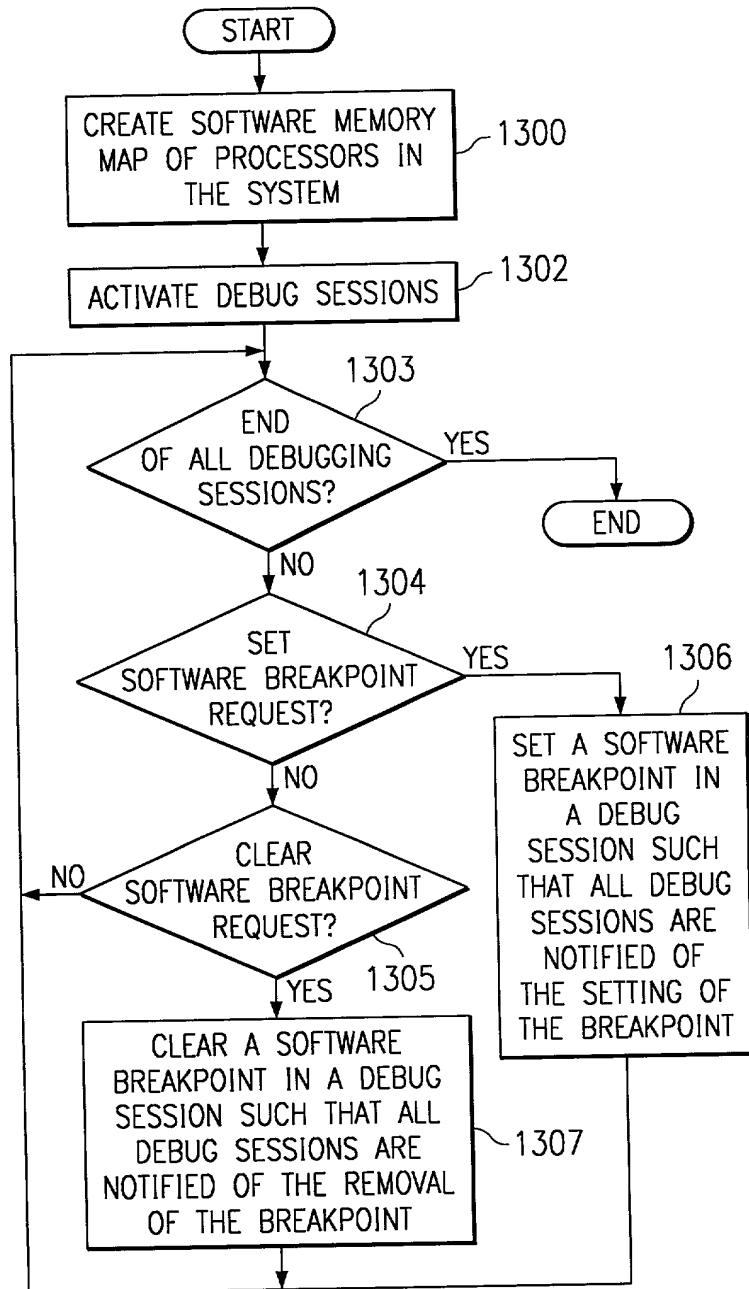


FIG. 13B

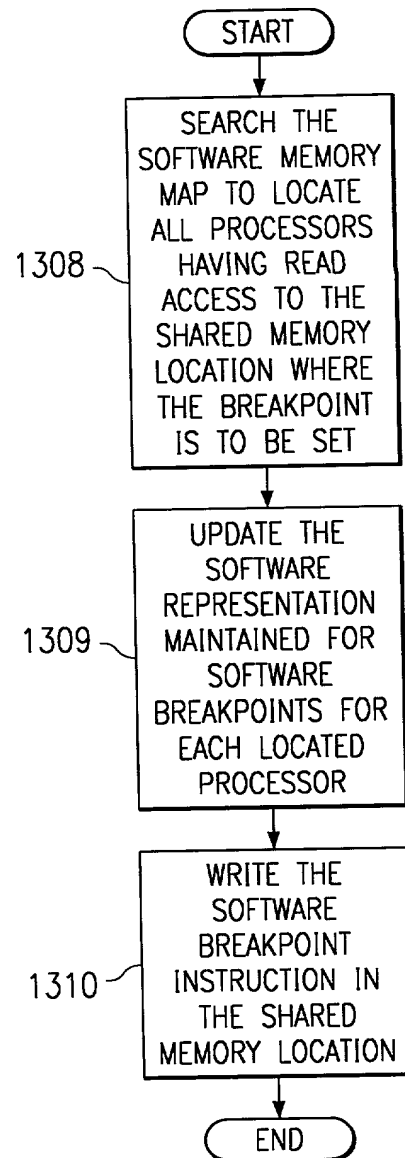
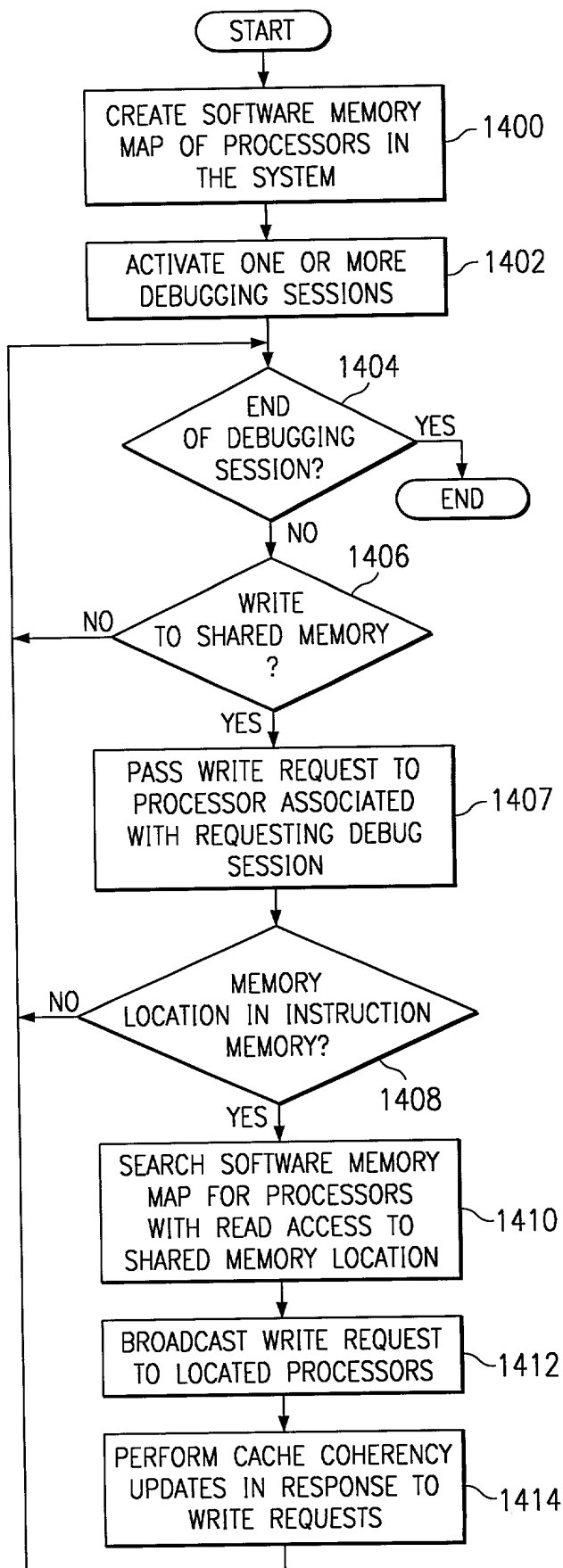
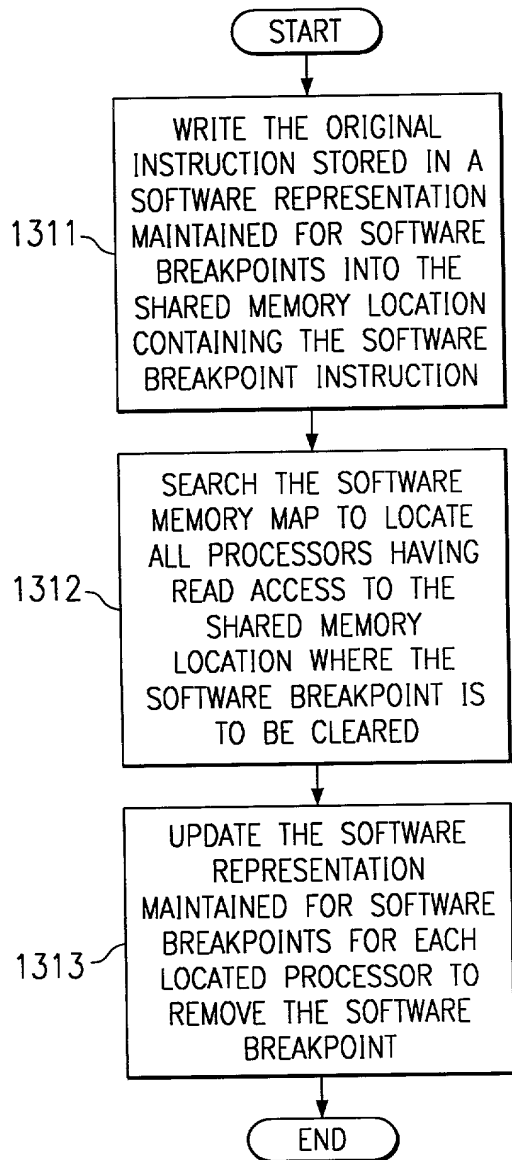


FIG. 13C



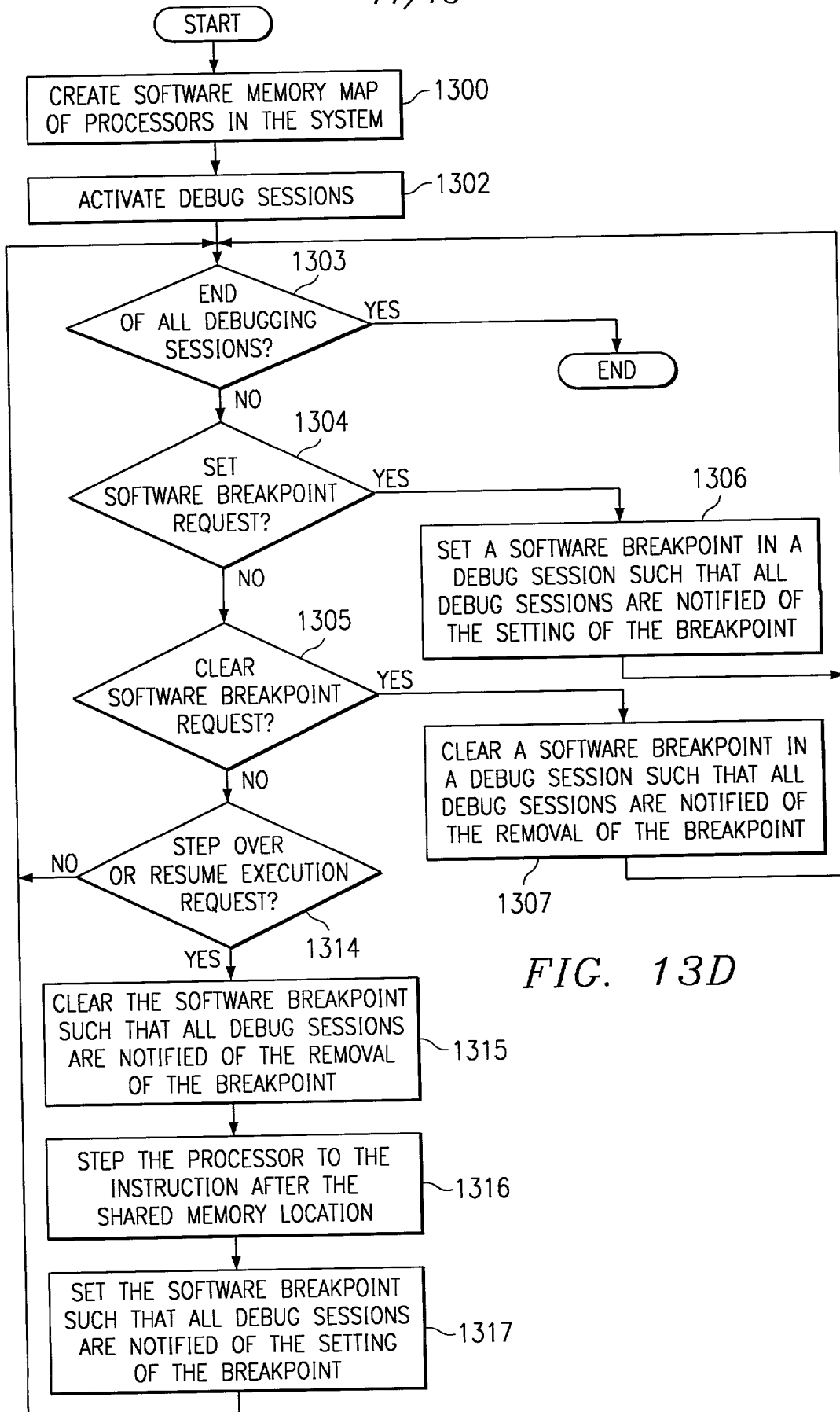


FIG. 13D

